MAR 0 6 2003 to

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Michael Thompson et al.

Attorney Docket No.: P67753US0

Serial No.: 10/088,096

Group Art Unit: 2818

Filed: July 2, 2002

Examiner: Connie C. YOHA

For:

SENSING DEVICE FOR A PASSIVE MATRIX MEMORY AND A READ METHOD FOR

USE THEREWITH

TRANSMITTAL

Commissioner of Patents and Trademarks Washington, D.C. 20231

Sir:

Transmitted herewith is an Amendment, Version with Markings to Show Changes Made and Petition for Extension of Time in the above captioned application.

\_\_\_\_Small Entity status of this application under 37 C.F.R. 1.9 and 1.27 has been established by a verified statement previously submitted.

The fee has been calculated as shown below:

Claims Remaining	Highest Number	Present Extra	Small Entity	Other Than A Small Entity
After Amendment	Previously Paid For		Rate Addit. (	or) Rate Addit. Fee
Total -		0	x09 = \$	x 18 = \$
Indep	- 3 =	0 .	x42 = \$	x 84 = \$
First Pre	esentation of			
Multiple Dependent Claim			x140 = \$	+280 = \$
Total Additional Fee			· \$	\$

XX A check in the amount of \$110.00 is attached for: Petition for Extension of Time

If a Petition for Extension of Time is necessary and the Petition and/or the check is not enclosed, this will act as the Petition and applicant herewith petitions the Commissioner to extend the time for response and charge any fees necessary under 37 CFR 1.17 (a)(1)-(5) to Deposit Account No. 06-1358. The Commissioner is also authorized to charge payment of any other additional fees associated with this communication or credit any overpayment to Deposit Account No. 06-1358. A duplicate copy of this sheet is attached.

JACOBSON HOLMAN, PLLC

Dated: March 6, 2003 400 Seventh Street, N. W.

Washington, D.C. 20004-2201 By:

JLS/dmt

Jonathan L. Scherer

Reg. No. 29,851



PATENT

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## **AMENDMENT**

BOX NON-FEE AMENDMENT. Commissioner for Patents Washington, D.C. 20231

Sir:

In response to the Office Action of November 6, 2002 (Paper No. 7), please amend the above-captioned application as follows:

## IN THE ABSTRACT:

Please amend the Abstract of the Disclosure as follows:



A sensing device for reading data stored in a passive matrix memory including memory cells in the form of ferroelectric capacitors, includes an integrator circuit for sensing the current response and a device for storing and comparing two consecutive read values, one of which is a reference value. In a read method for use with the sensing device a bit line is connected to the